

PAKISTAN AERONAUTICAL COMPLEX BOARD, KAMRA AVIATION DESIGN INSTITUTE

Applications are invited from the citizens of Pakistan for employment against following vacant posts (**on contract basis**) having requisite qualification, age limit and provincial quota as mentioned against the posts within 15 days of advertisement (Last Date: **05-Jul-2019**):

Only CVs are to be sent at the following address:

Orderly Room AvDI, AvRID Secretariat ARF PAC Kamra, Attock
(For any query call at 051-90990-2132)

SITUATIONS VACANT

Sr. No.	Name of Post & BPS	Qualification & Experience	Maximum Age Limit	Number of Posts	Regional/ Provincial Quota
1	Senior Design Engineer (BPS-18)	(a) MS in Aerospace / Avionics / Mechanical / Electronics Engineering from PEC recognized institute / university. (b) 02 years post qualification experience in relevant field from Govt. / Semi Govt. Organization or Firm of repute is preferred. (c) Desired skills: MATLAB, Programming in C, C++, Simulink, Control System Design, Hardware interfacing, Verlog, VHDL, 6 DOF Aircraft modeling & Simulation. (d) Excellent report writing and communication skills (e) Min CPGA 3.0 (f) Test & Interview.	35 Years	01	Merit

Sr. No.	Name of Post & BPS	Qualification & Experience	Maximum Age Limit	Number of Posts	Regional/ Provincial Quota
2	Machine Learning Senior Design Engineer (BPS-18)	(a) MS in Electrical / Computer Science from PEC recognized institute / university. (b) 02 years post qualification experience in relevant field from Govt. / Semi Govt. Organization or Firm of repute is preferred. (c) Experience of Machine Learning Algorithm, MATLAB, Python would be preferred. (d) Excellent report writing and communication skills (e) Min CPGA 3.0 (f) Test and Interview.	35 Years	02	Merit
3	Design Engineer (BPS-17)	(a) BS in Aerospace / Avionics / Mechanical / Electronics Engineering from PEC recognized institute / university. (b) 01 years post qualification experience in relevant field from Govt. / Semi Govt. Organization or Firm of repute is preferred. (c) Desired Skills : MATLAB, Programming in C, C++, Simulink, Control System Design & Hardware interfacing. (d) Min CPGA 3.0 (e) Test and Interview.	32 Years	01	Merit
4	Machine Learning Design Engineer (BPS-17)	(a) BS in Electrical / Computer Science from PEC recognized institute / university. (b) 01 years post qualification experience in relevant field from Govt. / Semi Govt. Organization or Firm of repute is preferred. (c) Experience of Machine Learning Algorithm, MATLAB, Python would be preferred (d) Min CPGA 2.8 (e) Test and Interview.	32 Years	01	Merit